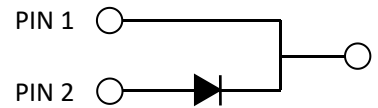
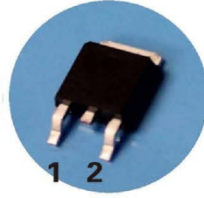


NOD02120E

Silicon Carbide Schottky Diode



Maximum Ratings ($T_c = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Value	Unit	Test Conditions	Note
V_{RRM}	Repetitive Peak Reverse Voltage	1200	V		
V_{RSM}	Surge Peak Reverse Voltage	1200	V		
V_{DC}	DC Blocking Voltage	1200	V		
I_F	Continuous Forward Current	9.5 5 2	A	$T_C=25^\circ\text{C}$ $T_C=125^\circ\text{C}$ $T_C=156^\circ\text{C}$	Fig. 7
I_{FRM}	Repetitive Peak Forward Surge Current	10	A	$T_C=25^\circ\text{C}$, $t_p=10$ ms, Half Sine Wave	
I_{FSM}	Non-Repetitive Peak Forward Surge Current	18	A	$T_C=25^\circ\text{C}$, $t_p=10$ ms, Half Sine Wave	
$I_{F,Max}$	Non-Repetitive Peak Forward Surge Current	180	A	$T_C=25^\circ\text{C}$, $t_p=10$ μs , Pulse	
P_{tot}	Power Dissipation	76.5 33.2	W	$T_C=25^\circ\text{C}$ $T_C=110^\circ\text{C}$	Fig. 6
T_J, T_{stg}	Operating Junction and Storage Temperature	-55 to +175	$^\circ\text{C}$		

Electrical Characteristics

Symbol	Parameter	Typ.	Max.	Unit	Test Conditions	Note
V_F	Forward Voltage	1.4 2.1	1.8 2.5	V	$I_F = 2$ A $T_J=25^\circ\text{C}$ $I_F = 2$ A $T_J=175^\circ\text{C}$	Fig. 1
I_R	Reverse Current	2 40	20 100	μA	$V_R = 1200$ V $T_J=25^\circ\text{C}$ $V_R = 1200$ V $T_J=175^\circ\text{C}$	Fig. 2
Q_C	Total Capacitive Charge	11.2		nC	$V_R = 800$ V, $T_J = 25^\circ\text{C}$ $Q_C = \int_0^{V_R} C(V)dV$	Fig. 4
C	Total Capacitance	148 11 8		pF	$V_R = 0$ V, $T_J = 25^\circ\text{C}$, $f = 1$ MHz $V_R = 400$ V, $T_J = 25^\circ\text{C}$, $f = 1$ MHz $V_R = 800$ V, $T_J = 25^\circ\text{C}$, $f = 1$ MHz	Fig. 3
E_C	Capacitance Stored Energy	5.8		μJ	$V_R = 800$ V	Fig. 5

Thermal Characteristics

Symbol	Parameter	Typ.	Unit	Note
$R_{\theta JC}$	Thermal Resistance from Junction to Case	1.96	$^{\circ}C/W$	Fig. 8

Typical Performance

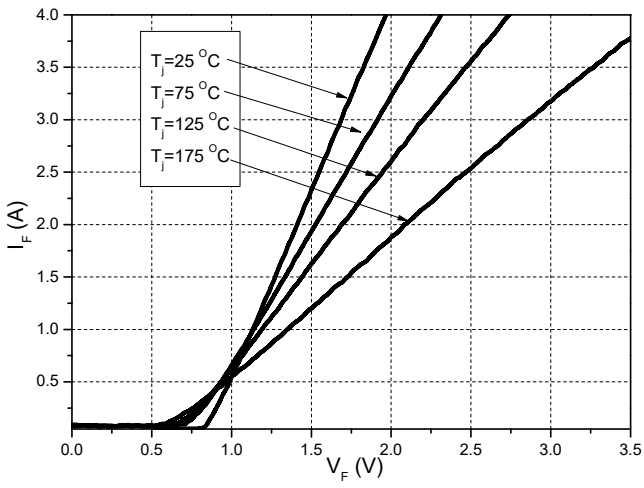


Figure 1. Forward Characteristics

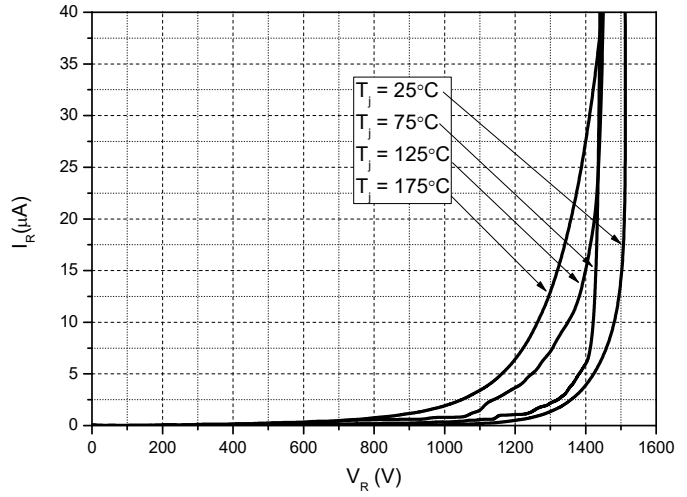


Figure 2. Reverse Characteristics

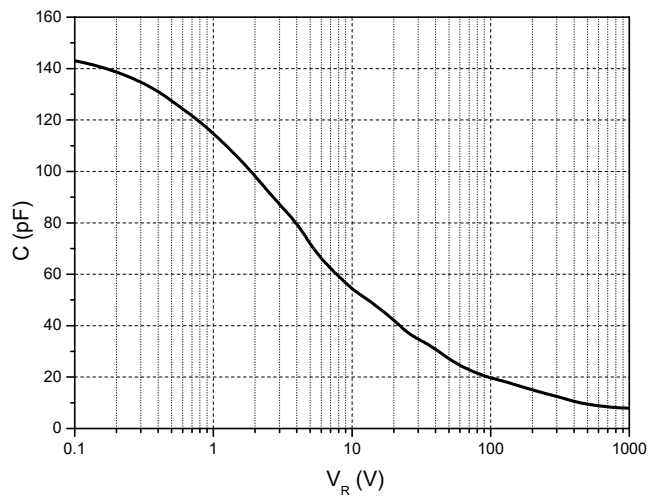


Figure 3. Capacitance vs. Reverse Voltage

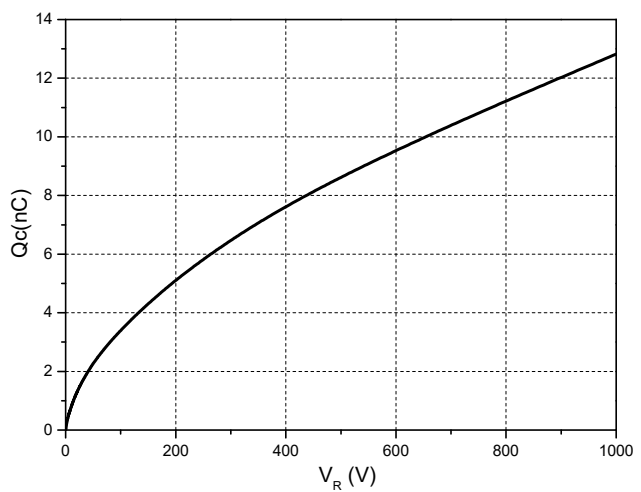


Figure 4. Total Capacitance Charge vs. Reverse Voltage

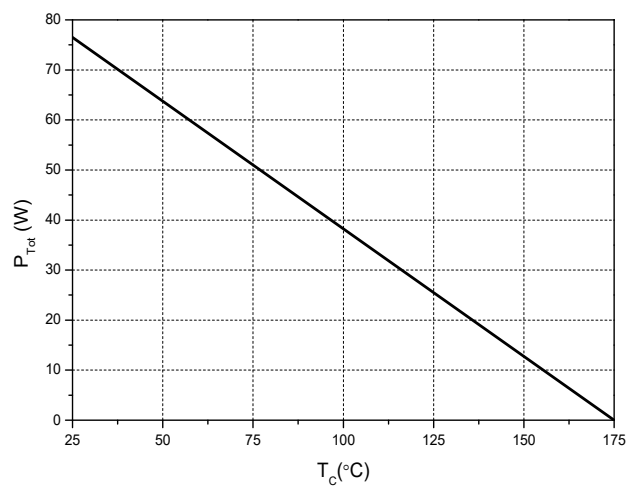
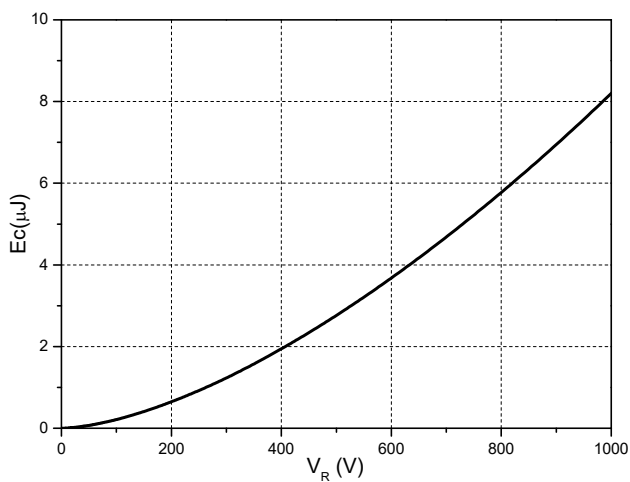


Figure 5. Capacitance Stored Energy

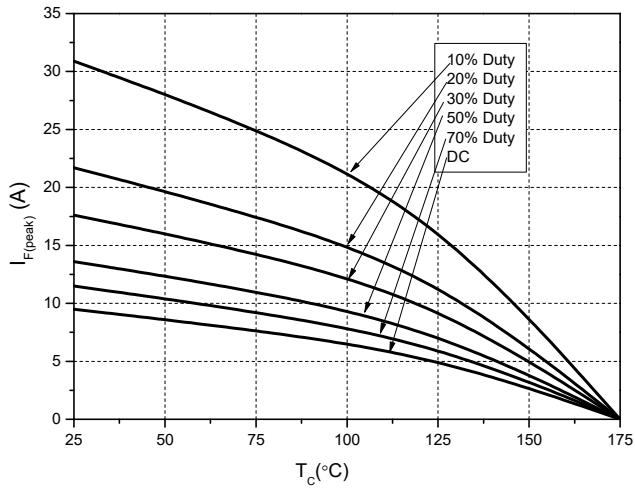


Figure 7. Current Derating

Figure 6. Power Derating

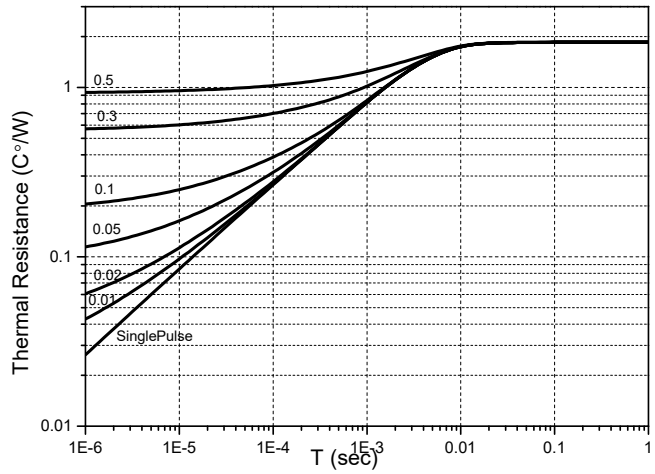


Figure 8. Transient Thermal Impedance

Package Dimensions: TO-252

