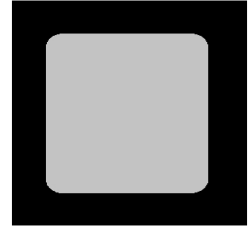


N3D-0650-002

Silicon Carbide Schottky Diode Chip



Part Number	Die Size	Anode	Cathode
SIC-0650-002	0.99 x 0.99 mm ²	Al	Ni/Ag

Maximum Ratings

Symbol	Parameter	Value	Unit	Test Conditions	Note
V _{RRM}	Repetitive Peak Reverse Voltage	650	V		
I _F	Continuous Forward Current	2	A	T _c =150°C	
V _R	DC Peak Blocking Voltage	650	V		
T _J , T _{stg}	Operating Junction and Storage Temperature	-55 to +175	°C		

Electrical Characteristics

Symbol	Parameter	Typ.	Max.	Unit	Test Conditions	Note
V _F	Forward Voltage	1.5 1.8	1.8 2.0	V	I _F = 2 A T _J =25°C I _F = 2 A T _J =175°C	Figure 1
I _R	Reverse Current	1 12	20 100	μA	V _R = 650 V T _J =25°C V _R = 650 V T _J =175°C	Figure 2
Q _C	Total Capacitive Charge	4.8		nC	V _R = 400 V, T _J = 25°C Q _C =∫ ₀ ^{V_R} C(V)dV	Figure 4
C	Total Capacitance	95 10 9		pF	V _R = 0 V, T _J = 25°C, f = 1 MHz V _R = 200 V, T _J = 25°C, f = 1 MHz V _R = 400 V, T _J = 25°C, f = 1 MHz	Figure 3
E _c	Capacitance Stored Energy	1.4		μJ	V _R = 400 V	

Mechanical Parameters

Parameter	Typ.	Unit
Die Size	0.99 x 0.99	mm
Anode Pad Size	0.73 x 0.73	mm
Anode Pad Opening	0.54 x 0.54	mm
Thickness	180 ± 10%	μm
Wafer Size	100	mm
Anode Metalization (Al)	4	μm
Cathode Metalization (Ni/Ag)	1.5	μm
Frontside Passivation	Polyimide	

Typical Performance

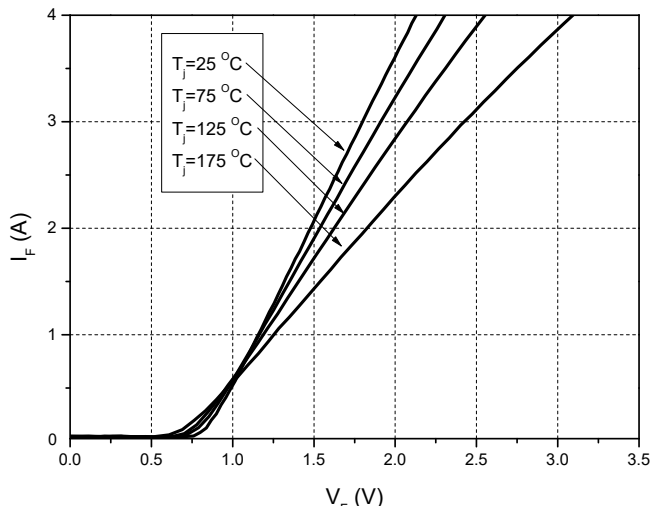


Figure 1. Forward Characteristics

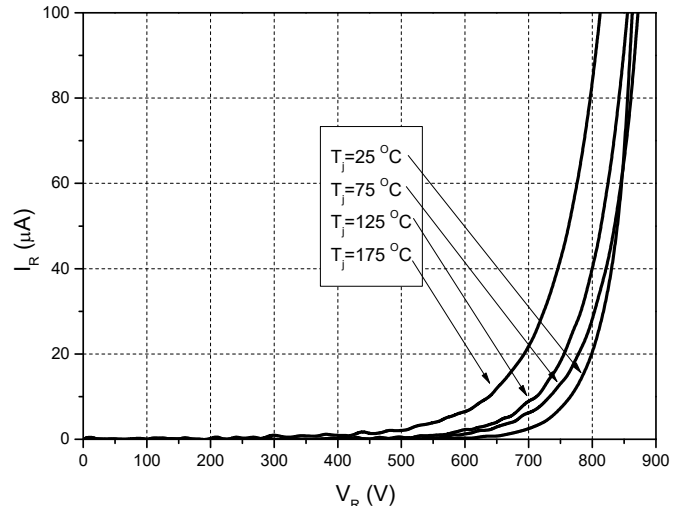


Figure 2. Reverse Characteristics

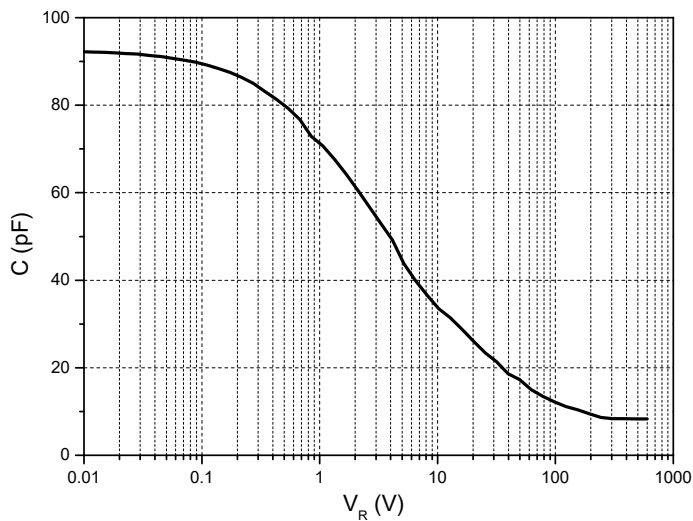


Figure 3. Capacitance vs. Reverse Voltage

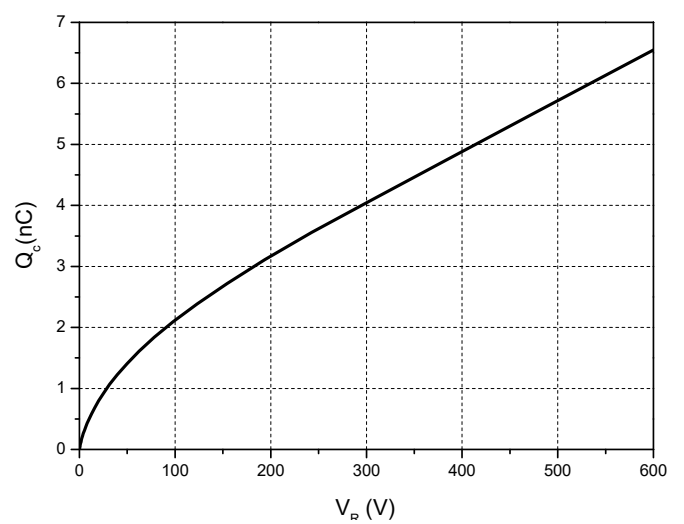
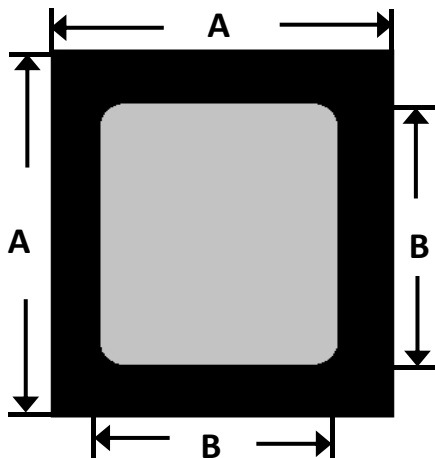


Figure 4. Total Capacitance Charge vs. Reverse Voltage

Chip Dimensions



Symbol	Dimension(mm)
A	0.99
B	0.54