

# **US1A THRU US1M**

# 1.0A SURFACE MOUNT ULTRA-FAST RECTIFIER

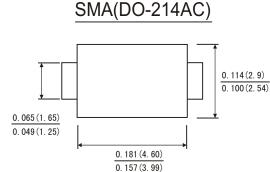
#### **Features**

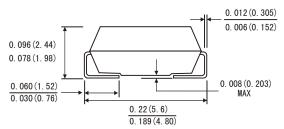
- Glass Passivated Die Construction
- Diffused Junction
- Ultra-Fast Recovery Time for High Efficiency
- Low Forward Voltage Drop, High Current Capability, and Low Power Loss
- Surge Overload Rating to 30A Peak
- Ideally Suited for Automated Assembly
- Plastic Material: UL Flammability Classification Rating 94V-0

#### **Mechanical Data**

- Case: Molded Plastic
- Terminals: Solder Plated Terminal Solderable per MIL-STD-202, Method 208
- Polarity: Cathode Band or Cathode Notch
- Marking: Type Number
- Weight: 0.064 grams (approx.)
- Mounting Position: Any







Dimensions in inches and (millimeters)

## Maximum Ratings and Electrical Characteristics T<sub>A</sub> = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic		Symbol	US1A	US1B	US1D	US1G	US1J	US1K	US1M	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage		V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>	50	100	200	400	600	800	1000	V
RMS Reverse Voltage		V <sub>R(RMS)</sub>	35	70	140	280	420	560	700	V
Average Rectified Output Current @ T <sub>T</sub> = 75°C		lo	1.0							Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave Superimposed on Rated Load (JEDEC Method)		I <sub>FSM</sub>	30						А	
Forward Voltage Drop	@ I <sub>F</sub> = 1.0A	$V_{FM}$		1.0		1.3		1.7		V
	© T <sub>A</sub> = 25°C © T <sub>A</sub> = 100°C	I <sub>RM</sub>				5.0 100				μА
Reverse Recovery Time (Note 2)		t <sub>rr</sub>	50				75		ns	
Typical Junction Capacitance (Note 1)		Cj	20 10					pF		
Typical Thermal Resistance, Junction to Terminal		$R_{\theta JT}$	30						°C/W	
Operating and Storage Temperature Range		T <sub>j</sub> , T <sub>STG</sub>	-65 to +150							°C

Notes: 1. Measured at 1.0MHz and applied reverse voltage of 4.0V DC.

2. Measured with  $I_F = 0.5A$ ,  $I_R = 1.0A$ ,  $I_{rr} = 0.25A$ .

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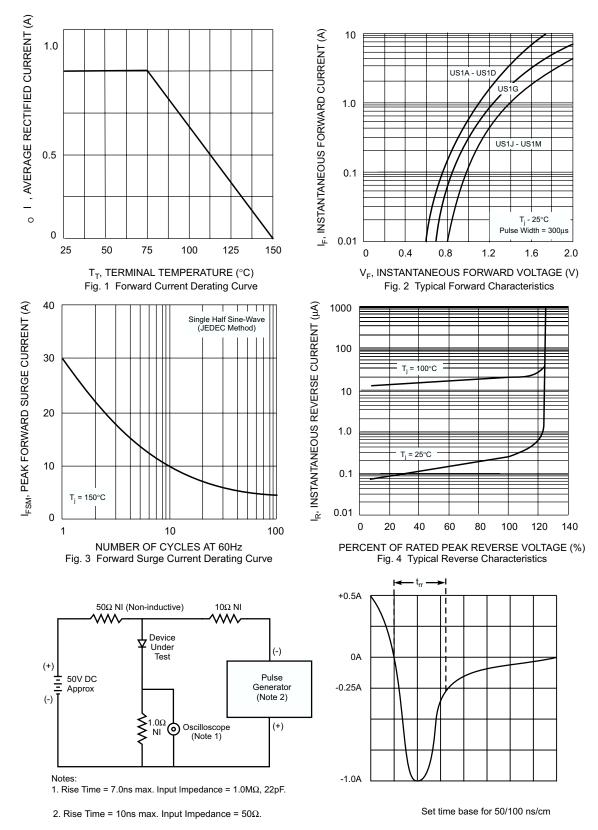


Fig. 5 Reverse Recovery Time Characteristic and Test Circuit